

Visible Light Communication Kits for Education

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Abstract

The development of educational kit must be compiled on how to prepare undergraduate students in the engineering field in following the new trends globally and becoming an alternative to the technical education system based on practical approach. The primary motivation for this research is to design and implement the visible light communications (VLC) educational toolkit, especially in the analog front-end part. It consisted of six kits (transimpedance amplifier, pre-amplifier, DC-offset remover, analog filter, and AGC) in which each kit has one practicum task. There are six tasks and one task for the project by integrating these kits. The undergraduate students can use this educational kit to investigate the physical layer in a VLC system. It provides a low-complexity educational kit, so this is becoming an alternative as supplement course offered in this field. Then, it has a simple design and user-friendly.

Keywords: Educational kits, Li-Fi, Visible light communication

1. Introduction

The research on VLC is becoming a prevalent topic in global, and many R&D activities have been carried-out towards developing the VLC systems for indoor/outdoor applications, targeting for high- or low-rate data transmission purpose. M. Figueiredo et al. (2017: 28-37) reported that VLC as the one of the new trend in the field of telecommunications over last five years from 2010 to 2015, viewed from Google Scholar search engine and IEEE *Xplore* online library. In regards to 'research trends of VLC.' We re-investigate to update the data where its report is shown in Figure 1. The data have obtained from two popular databases above. We can observe it in the charts that the number of publications tending to increase.

Figure 2 depicts a VLC system consists of the transmitter, visible light link (modulated light) and the receiver. The emitter of VLC usually uses the light source (*e.g.*, LEDs, LCD screen, except a fluorescent and incandescent) as a modulation device on the optical carrier at the visible light spectrum. While the VLC receiver is composed of photosensors (*e.g.*, photodiode,



LDR, phototransistor, etc.) or image sensor. The next stage is amplifiers, signal recovery circuit, filters, and also auto-gain circuit. The VLC system was designed to employ direct detection based on LED intensity (C.G. Lee et al., 2011: 328-338).



Figure 1. The illustration of research trend over the last five years by visualizing the number of search results for the search term [*allintitle: Visible light communications*] and ["*Visible light communication*"]. It was conducted using the Google scholar engine, then with search term [*visible light communications* & *visible light communication*] conducted using the IEEE *Xplore* digital library, counted on Feb. 1, 2018, 05.00 PM



Figure 2. The structure of VLC system consisting Analog transceiver, an optical link, and transceiver device. The illustration was edited from C. Windlin (2017)

In respect of VLC development, there are several technology companies of VLC developers (new-startup company) as global market key players includes: PureLiFi Ltd. (UK), Koninklijke Philips N.V. (Netherland), General Electric Co. (U.S.), Panasonic Corp. (Japan), LVX system (Canada), Oledcomm (France), IBSENtelecom (Norway), etc. While the Disney research (Switzerland) focuses on the VLC implementation for toys. Given the popularity of this research scope area, the VLC systems need to be adopted by higher educations in their

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curriculum development to keep up continuously with the global research trend. Some have been performed-out concerning the adoption of VLC technology into the related courses that includes three levels of learning standards: elementary levels such as presented by C. Windlin (2017), L.B. Oliveira et al. (2017: 105-111), C. Windlin & J. Laaksolahti (2017) and intermediate levels. Even to the advanced level that proposed by particular research groups such as reported by V. Vasinek et al. (2009) and A.M. Johnson et al. (2009).

However, following with our observations in the significant scientific database, there are still many colleges & universities that either rarely to provide a practical approach in exploring the analog front-end circuitry for the VLC application. The related research found that proposed by A.A. Akram and J. Hill (2011: 4-9) and Q. Wang et al. (2015) but they focus on fiber optic communication or FSO, not for VLC purpose. Later, Q. Wang et al. (2015) introduces VLC kits for education purpose but focus on communication protocol (digital section), not in analog section. The analog part in VLC system has a vital role in processing the analog signal (S. Fuada, 2017).

According to the brief description of research backgrounds, so the primary motive for this work was the need for a low-complexity laboratory alternative for lecturing the undergraduate student in VLC system focusing on the analog part to additional courses covered in this topic. This VLC educational toolkit provides undergraduate students with a new way to study the physical layer of simple VLC systems. As stated before, this proposed kits can be used as the supplement courses offered in this field. Such as those associated with Op-Amps (*e.g.*, sensors and transducers course, analog instrumentation, etc.), correlated with wireless communication (*e.g.*, signal and processing course, emerging communication system, optical communication, etc.).

2. Methodology

In this section, we describe the design of educational kits for low-cost of VLC system for low-speed application, the number of the designed kits, and how to use these kits in practical-course. Firstly, we present the design of the kit. Since this course focuses on the low-speed VLC application, so there is no specific consideration in choosing the materials, as reported by L.B. Oliveira et al. (2017: 105-111).

"In a teaching laboratory environment, it is difficult to build circuits for high operating frequencies because this would require the design and manufacture of high-quality printed circuit boards with low parasitic capacitances. The only option is to select circuits and systems where the signal frequency is low so that high parasitic capacitances have a negligible impact on performance".

Therefore, to fit with the low-complexity target, we develop the educational kit separately. Thus it can be practiced by plug-and-play as needed (portable kits) and also it can be evaluated per-block easily (user-friendly). The concept of educational kit for VLC depicted in Figure 3, that is analog front-end (AFE) in which each kit has three input channels for the supply voltage, *i.e.*, +5 V_{DC}, ground (GND), and -5 V_{DC}. Whereas the ports on input and output, there are five channels, namely: +5 V_{DC}, GND, -5 V_{DC}, data and GND.





Figure 3. The design concept of low-complexity educational kits for low-speed VLC system





Figure 4. The design concept of low-complexity educational kits for VLC





Figure 5. PCB layout of educational kits for VLC consisting transimpedance amplifier kit: (a) significant size wrapped; (b) small size packaged viewed on the bottom layer; (c) small size packaged viewed on the top layer, (d) Summing Amplifier kit; the Noninverting Amplifier kit: (e) for Op. Amp with one channel; (f) for Op. Amp with two channels; Inverting Amplifier kit: (g) for Op. Amp with one channel; (h) for Op. Amp with two channels; (i) Analog Filter kit; (j) AGC kit



The primary circuits for designing the VLC system are shown in Figure 4. It contains: (1) a transimpedance amplifier circuit with photoconductive mode (Figure 4a); (b) Negative noninverting summing amplifier (Figure 4b); (c) some researchers use a differential amplifier circuit to represent the function as a DC-offset remover; (d) Noninverting (Figure 4c) or Inverting configurations (Figure 4d) to describe the role as voltage amplifier; (e) analog filter with second order HPF (Sallen-key topology) as depicted in Figure 4d, some researchers use the LPF, BPF, or BSF depending on the filtered frequency specification, (f) and additional parts in the analog section of VLC system, *e.g.* AGC.

All of the above circuits realized into double layer PCB that refer to Figure 3 that was developed by using PROTEUS 7.0 as depicted in Figure 5 (Please see in the Appendix section). Afterward, we generate the PCB layout into 3D view in ARES© as shown in Figure 6. These circuits are then printed-out and mounting its electronic components.



Figure 6. PCB of educational kits for VLC

3. Results and Discussions

Figure 7 shows the implementation kits from Figure 6. Several experiments were developed and implemented using the educational VLC kits, these experiments described in Table 1. This lab sequence used for undergraduate student level. We arranged in a bottom-up course based. The coverage includes: (0) Introduction; (1) converting light to voltage using transimpedance amplifier; (2) Gain investigation and its bandwidth measurement using inverting and noninverting amplifier; (3) experiment for compensating the DC signal in receiver front-end; (4) interference filtering using HPF; (5) trial for auto-gain employing IC LM13700; (6) real-time audio transmission wireless system, without the use DSP, utilizing all educational VLC kits as final exam; and (7) final/last project.





Figure 7. A photograph VLC educational kits

Table 1. Several ex-	periments of	ffered to r	practice the	proposed	VLC educational kits
		merea to p		proposed	

Activities	Kit to be used	Description
Introduction 3 Hours expected	None	This activity explains the operation of the essential electronic components for low-complexity VLC system, such as Op Amp, photodiode, LED, and the transistor (both bipolar and field effect), and how to use them in practical yet straightforward circuits. The undergraduate students also learn to design more complex courses, such as how to analyze circuits employing Op Amp configuration. Then learn how the VLC works and building the blocks. Later, the lecturer gives the introduction how to use the educational kits in each task. Identify and use measurement tools in the experiment that is a digital oscilloscope, signal generator, and a frequency analyzer. The last is the software used in which for this work we used TINA TM
Experiment I 6 Hours expected	Transimpedance Amplifier (Figure 4a)	There are several types of signal amplification circuits on the first stage of VLC system. One of them is The transimpedance amplifier that is simple to implement. It provides current-to-voltage (I-V) conversion by using shunt R_f around an inverting amplifier (S. Fuada et al., 2016: 31-35). In this experiment, the students were required to prepare tools and materials. <i>i.e.</i> , digital oscilloscope, signal generator, different Op Amps at least five types (<i>e.g.</i> , OPA380, OPA620, TL072, AD8011AN, LM358, etc.). Then different low-cost photodiodes at least three types (<i>e.g.</i> , SP-8ML, BPX65, S1227-66BK, etc.). The last one is LED driver to transmit sinusoid signal with linear topology (S. Fuada et al., 2016: 371-375) or square signal with switch topology of the LED



		driver (T. Adiono et al., 2016: 319-327). The tasks offered is the student must measure the bandwidth of the transimpedance amplifier by using a combination of photodiodes and Op Amps, both based on the simulation or real practice. The answer key for this testing is presented by S. Fuada (2017) and T. Adiono et al. (2017: 1375-1380).
Experiment II 5 Hours expected	Noninverting amplifier (Figure 4c) and Inverting amplifier (Figure 4d)	The next experiment is amplifier circuit test using noninverting and inverting configuration. To complete the investigation, students must understand how to calculate the <i>ac</i> gain of the circuit to obtain the desired performance.
Experiment III 2 Hours expected	DC-offset remover (Figure 4b)	The optical wireless communication system including VLC is vulnerable to the ambient light source that generates DC signal like a flashlight, sunlight, or LED lamp (S. Fuada et al., 2017: 14-30) (T. Adiono et al., 2016: 581-584). Therefore, it is crucial to involve appropriate analog circuit to reject unwanted DC noise because it can make signal distortion (clipped signal) in the VLC receiver. The measurement tools used a digital oscilloscope to know the behavioral of the received signal before and after filtering, while the components used DC lamp. The answers key for this testing are presented by T. Adiono (2017: 616-619).
Experiment IV 6 Hours expected	Analog Filter (Figure 4f)	The VLC system also suffers from the other illuminations like fluorescent and incandescent because it can generate 100 Hz to 150 Hz of the carrier frequency (T. Adiono et al., 612-615) so it is also essential to employ an appropriate analog filter to reject unwanted noise in the recovered data signal. Frequency analyzer is used to analyze the SNR of the noise signal.
Experiment V 3 Hours expected	AGCIn this experiment, the students test the AGC circuit using LM13700 to maintain the voltage gain against the changing the input amplitude as well as input frequency. The answer k for this testing is presented by S. Fuada (2017).	
Experiment VI	All educational kits	In the last experiment, students build all circuits to consolidate the theoretical knowledge acquired in lectures and to learn the practical aspects. The audio signal generated by MP3 or <i>audio-out</i> of personal computer/laptop is chosen as an input signal, while the output of AGC circuit connects to the load (8 Ohm loudspeaker). The methods of real-time transmission are by playing the music and immediately heard on the speaker. Figure 8(a) shows the setting of this experiment.
Projects	All educational kits	Advanced students ready for an inductive approach to use this kits in their projects such as Li-Fi application as shown in Figure 8(b)





Figure 8. (a) Audio transmission system (S. Fuada & T. Adiono, 2017: 352-360), (S. Fuada et al., 2017: 398-403); (b) educational kits for the Li-Fi application (T. Adiono et al., 2017), (T. Adiono et al., Unpublished)

4. Conclusion and Future Work

We have proposed each kit to build VLC system for undergraduate student practicum. These educational kits present a low-cost alternative for educators to give a lecture the physical layer of a low-speed VLC application. We also provide the tasks for a reference experiment in which the students may build on. For future works, we will provide the DSP board such as general microcontroller or FPGA so the BER analysis of the experiment can be involved. Moreover, it is essential to include the eye diagram within the practicum for VLC system.

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